

October 22-24, 2014

ANA Crowne Plaza Kyoto, Japan

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SCOPE

- 1) Processes (Thermal, UV, R to R, others)
- 2) Large area and high throughput approaches
- 3) Materials (Resist, Anti-sticking)
- 4) Template
- 5) Tools
- 6) Modelling, simulation and inspection
- 7) Related and emerging methods (soft lithography, self-assembly, others)
- 8) Applications (Electronics, Integrated circuit, Optics, Bio, Energy, other micro-nano devices)

PLENARY SPEAKER

Stephen Y. Chou (Princeton Univ., USA)

Osamu Sudo (METI, Japan)

INVITED SPEAKERS

Graham Cross (CRANN Inst., Ireland)

Sachiko Kobayashi (Toshiba, Japan)

Stéfan Landis (LETI, France)

Jaslyn Law (A*STAR, Singapore)

Heon Lee (Korea Univ., Korea)

Yung-Chun Lee (National Cheng Kung Univ., Taiwan)

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Paper
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